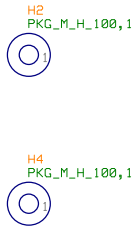
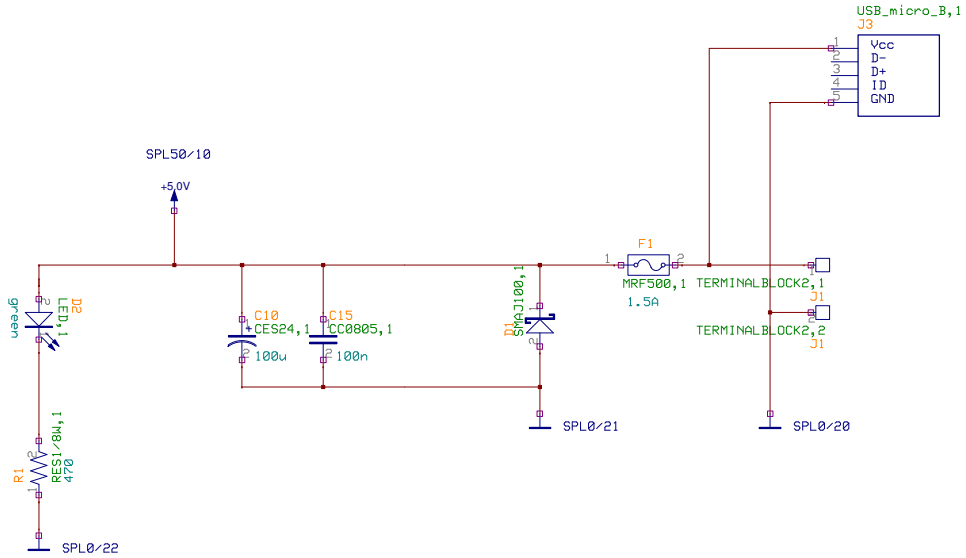


computermuseum muenchen		
Title:	Board mounting holes	
Project:	RL Disk Emulator	
Author:	A.Mann	
Date:	07.03.2017	Page:



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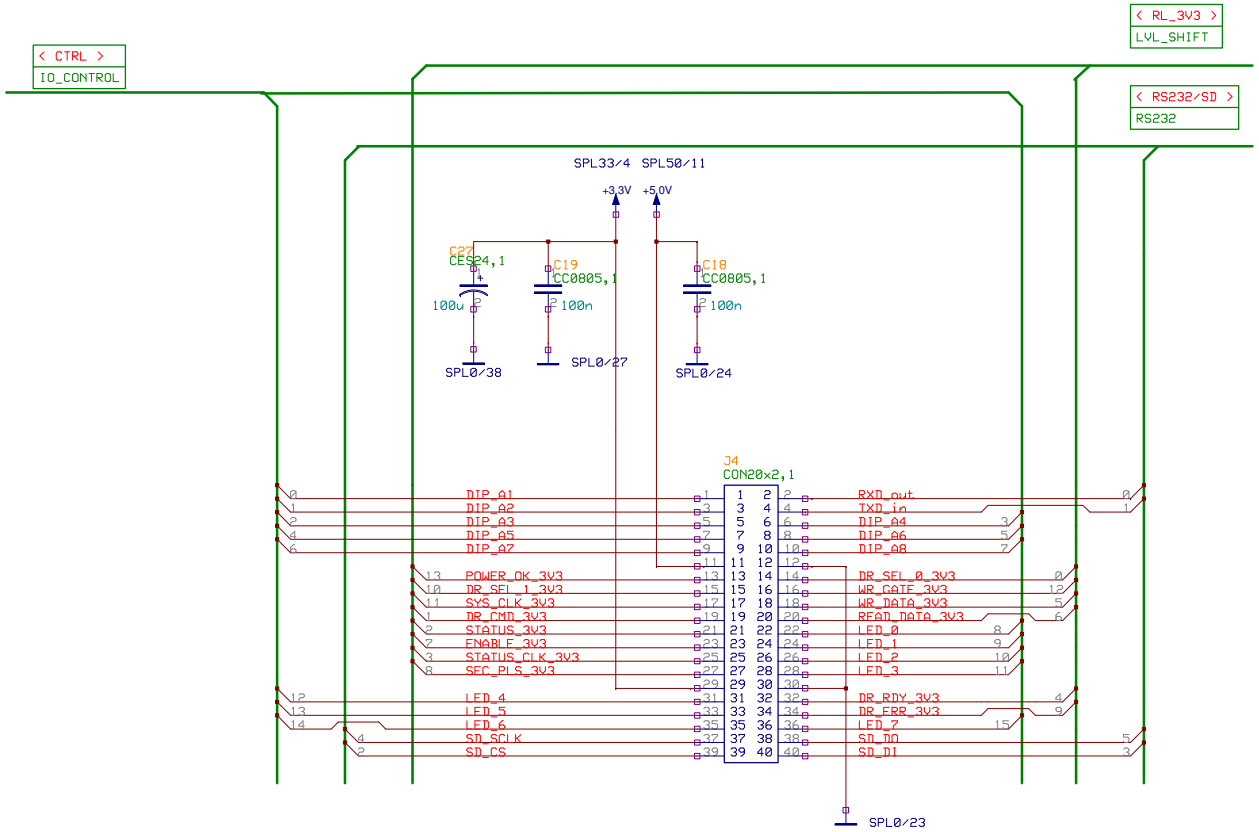
Title:	Power Input	
Project:	RL Disk Emulator	
Author:	A.Mann	
Date:	07.03.2017	Page:



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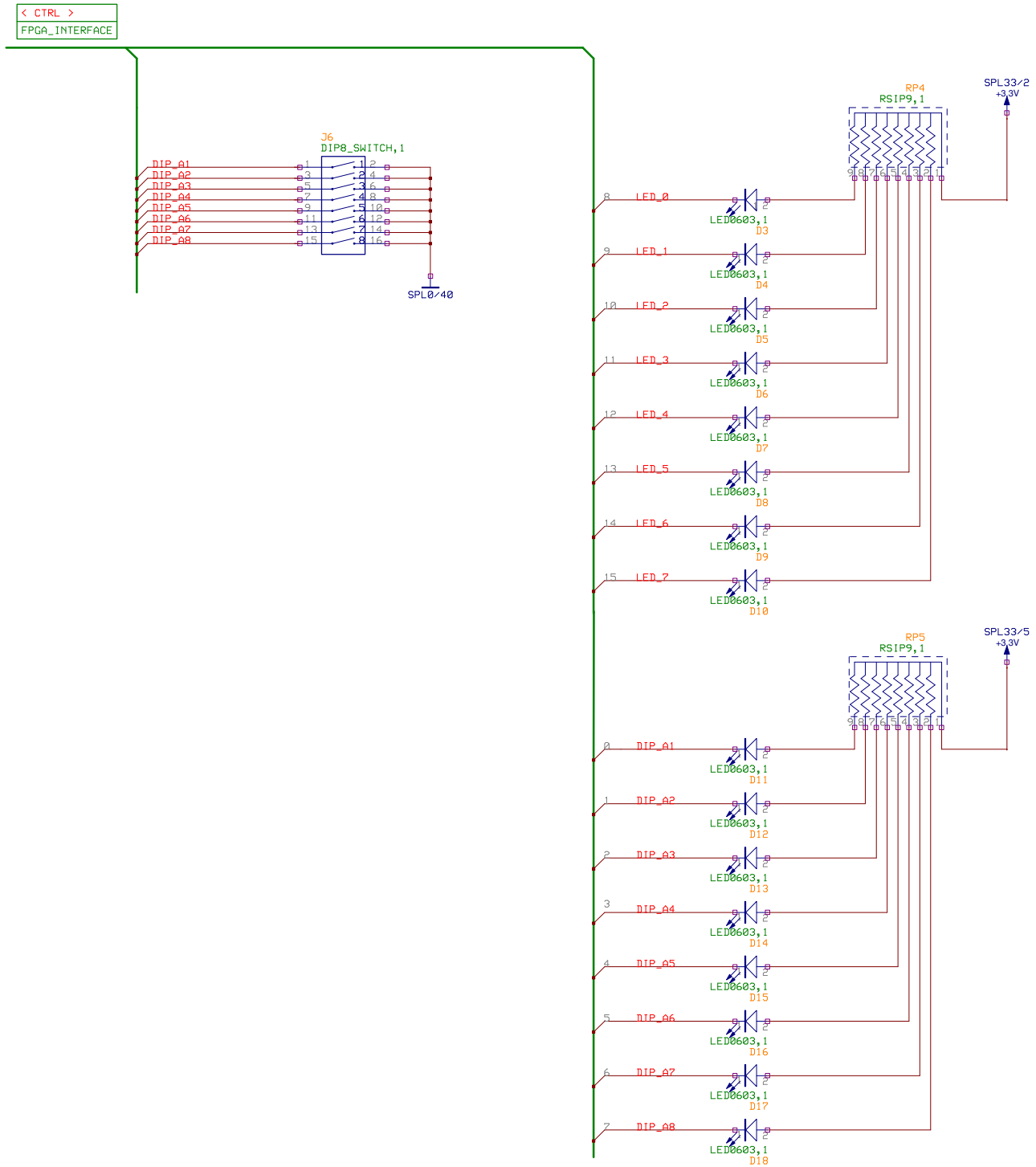
Title:	FPGA Interface	
Project:	RL Disk Emulator	
Author:	A.Mann	
Date:	07.03.2017	Page:



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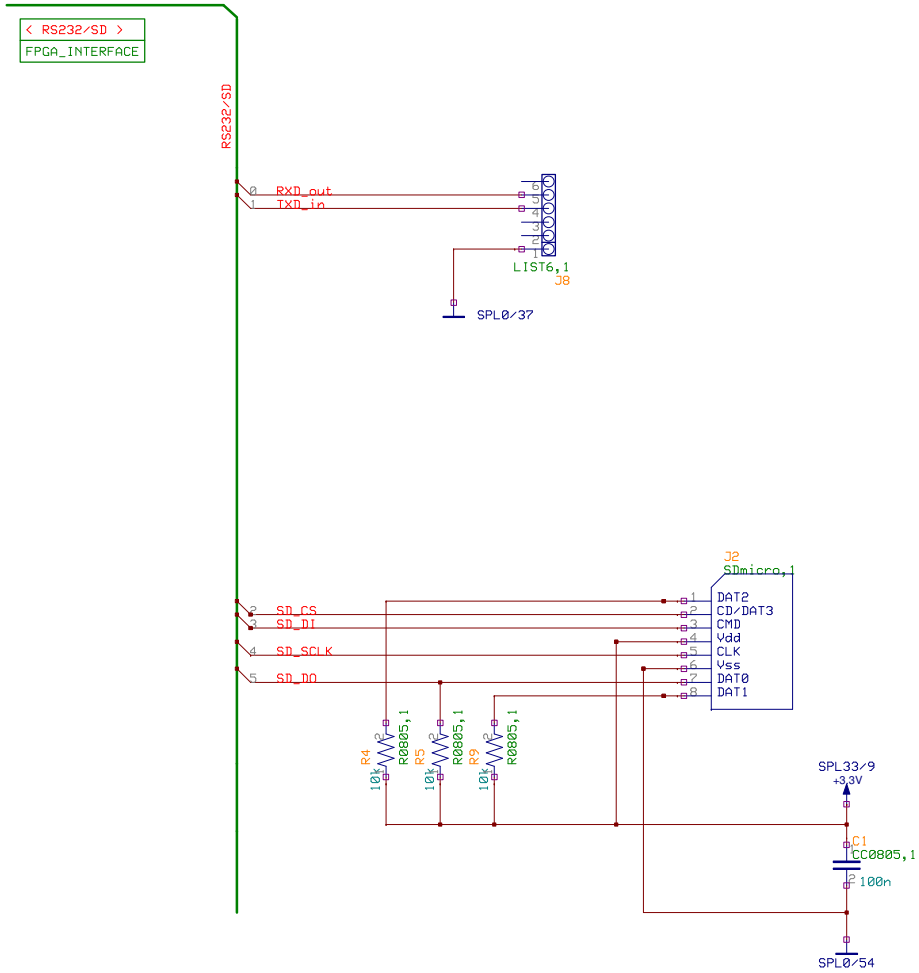
Title:	Control Switches	
Project:	RL Disk Emulator	
Author:	A.Mann	
Date:	07.03.2017	Page:



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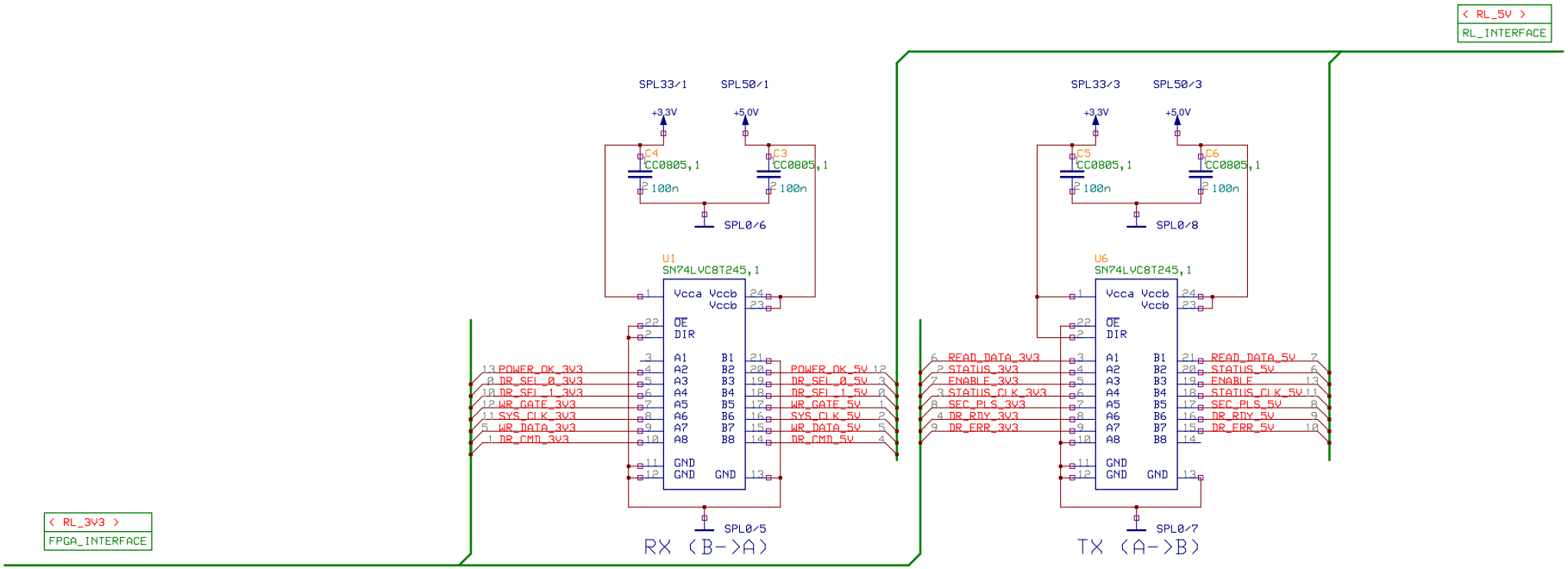
Title:	RS232 Interface	
Project:	RL Disk Emulator	
Author:	A.Mann	
Date:	07.03.2017	Page:



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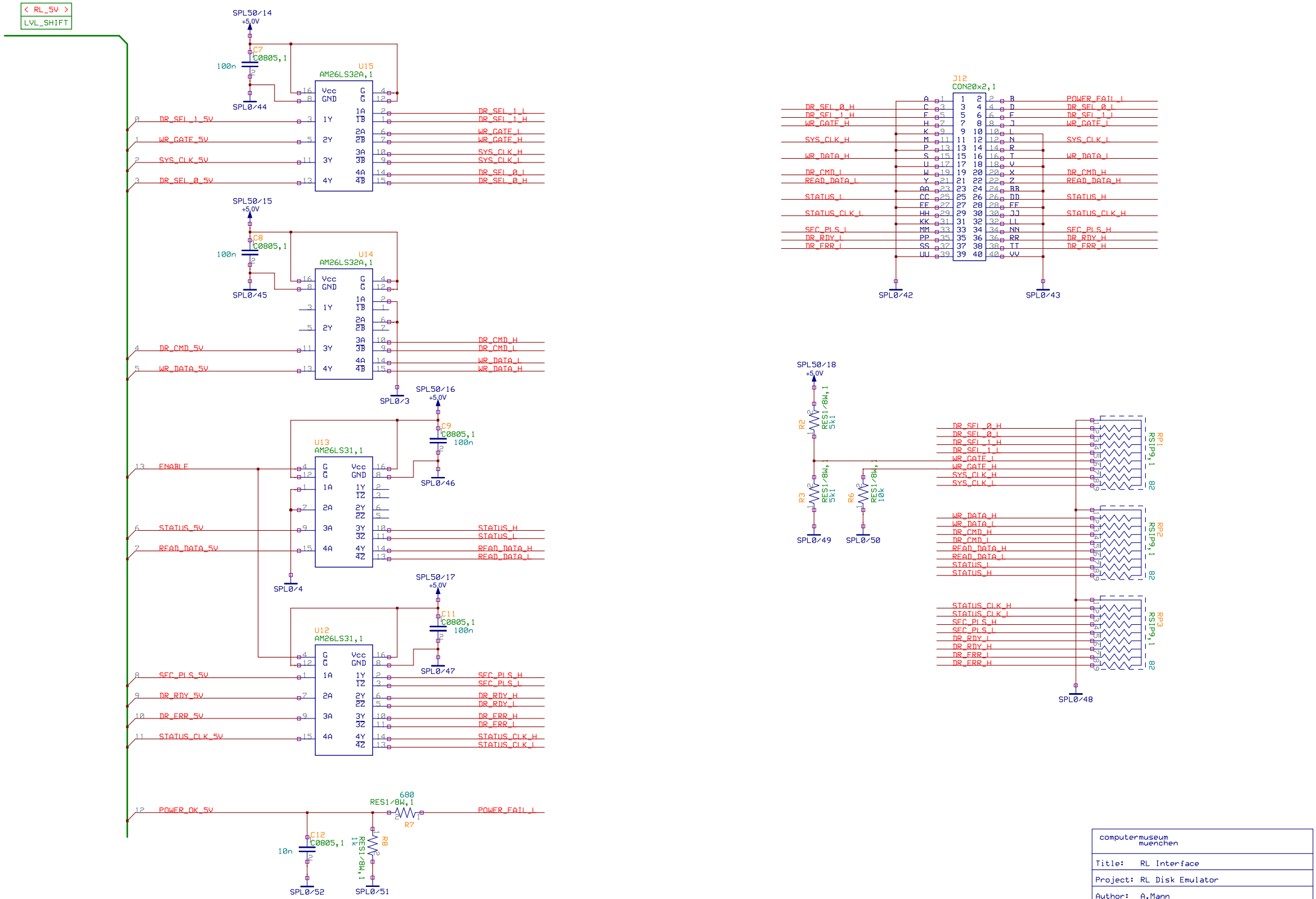
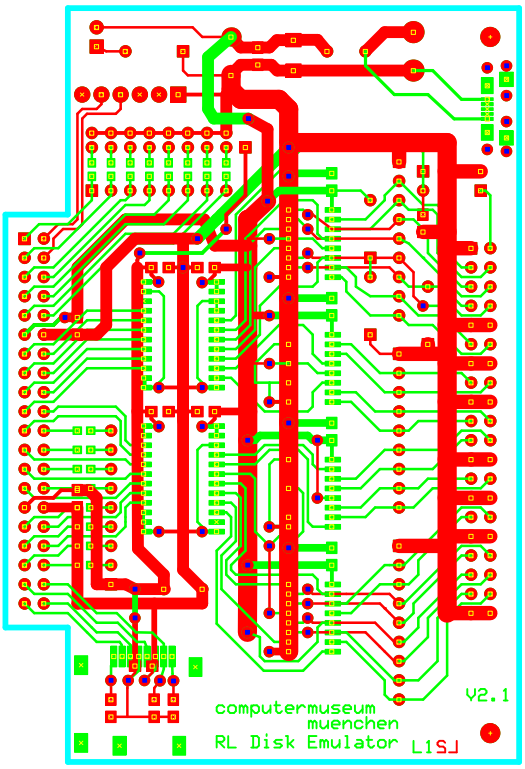
Title:	Level Shifters	
Project:	RL Disk Emulator	
Author:	A.Mann	
Date:	07.03.2017	Page:


RL_5V
LVL_SHIFT
SPL50/14
+5.0V
C7
C0805,1
100n
SPL0/44
U15
AM26LS32A,1
Vcc
GND
1Y
2Y
3Y
4Y
DR_SEL_1_5V
WR_GATE_5V
SYS_CLK_5V
DR_SEL_0_5V
DR_SEL_1_L
DR_SEL_1_H
WR_GATE_L
WR_GATE_H
SYS_CLK_H
SYS_CLK_L
DR_SEL_0_L
DR_SEL_0_H
SPL50/15
C8
SPL0/45
U14
DR_CMD_5V
WR_DATA_5V
DR_CMD_H
DR_CMD_L
WR_DATA_L
WR_DATA_H
SPL0/3
SPL50/16
C9
SPL0/46
U13
ENABLE
STATUS_5V
READ_DATA_5V
STATUS_H
STATUS_L
READ_DATA_H
READ_DATA_L
SPL0/4
SPL50/17
C11
SPL0/47
U12
SEC_PLS_5V
DR_RDY_5V
DR_ERR_5V
STATUS_CLK_5V
SEC_PLS_H
SEC_PLS_L
DR_RDY_H
DR_RDY_L
DR_ERR_H
DR_ERR_L
STATUS_CLK_H
STATUS_CLK_L
POWER_OK_5V
POWER_FAIL_L
680
RES1/8W,1
R7
C12
10n
R8
1K
SPL0/52
SPL0/51
J12
CON20x2,1
DR_SEL_0_H
DR_SEL_1_H
WR_GATE_H
SYS_CLK_H
WR_DATA_H
DR_CMD_L
READ_DATA_L
STATUS_L
STATUS_CLK_L
SEC_PLS_L
DR_RDY_L
DR_ERR_L
POWER_FAIL_L
DR_SEL_0_L
DR_SEL_1_L
WR_GATE_L
SYS_CLK_L
WR_DATA_L
DR_CMD_H
READ_DATA_H
STATUS_H
STATUS_CLK_H
SEC_PLS_H
DR_RDY_H
DR_ERR_H
SPL0/42
SPL0/43
SPL50/18
+5.0V
R2
R3
R6
RES1/8W,1
5k1
10k
SPL0/49
SPL0/50
RP1
RP2
RP3
RSIP9,1
82
SPL0/48
computermuseum muenchen
Title: RL Interface
Project: RL Disk Emulator
Author: A.Mann
Date: 07.03.2017
Page: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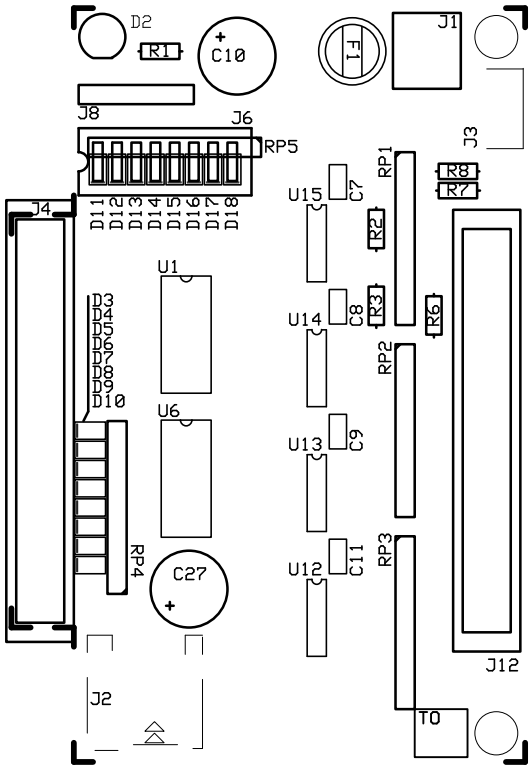
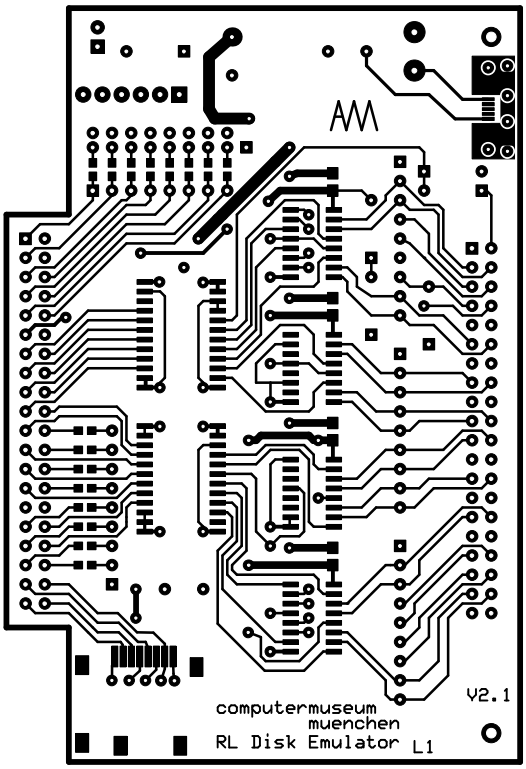
RL Disk Emulator
V2.1


D2
R1
C10
F1
J1
J8
J6
RP5
J3
RP1
R8
R7
U15
C7
R2
J4
D11
D12
D13
D14
D15
D16
D17
D18
U1
D3
D4
D5
D6
D7
D8
D9
D10
U14
C8
R3
R6
RP2
U6
U13
C9
RP4
C27
U12
C11
RP3
J12
J2
TO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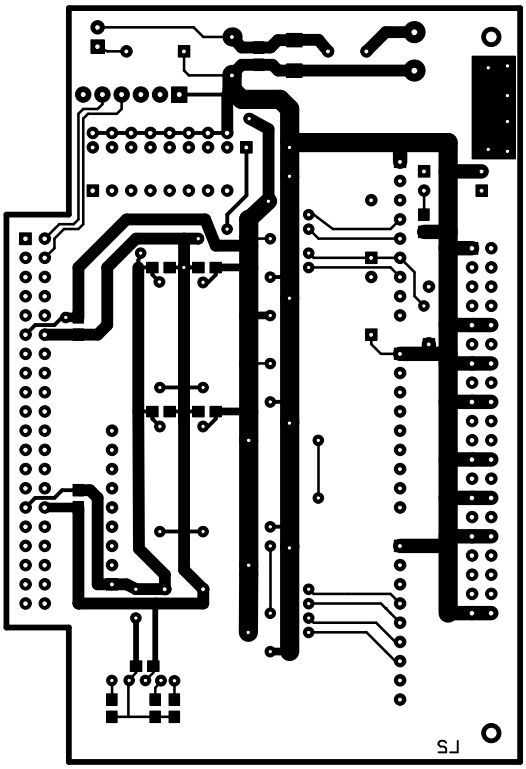
RL Disk Emulator
L1
V2.1



D1
C15
C12
C3
C4
C18
C6
C5
C19
C1
R9
R5
R4
B0