H1 PKG_M_H_100,1

H2 PKG_M_H_100,1

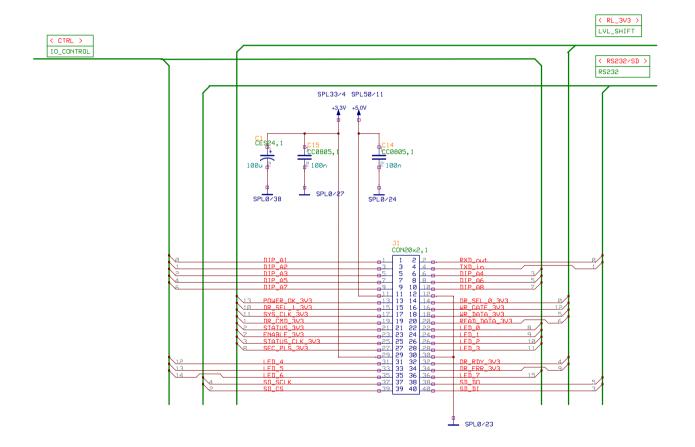
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Title: Board mounting holes

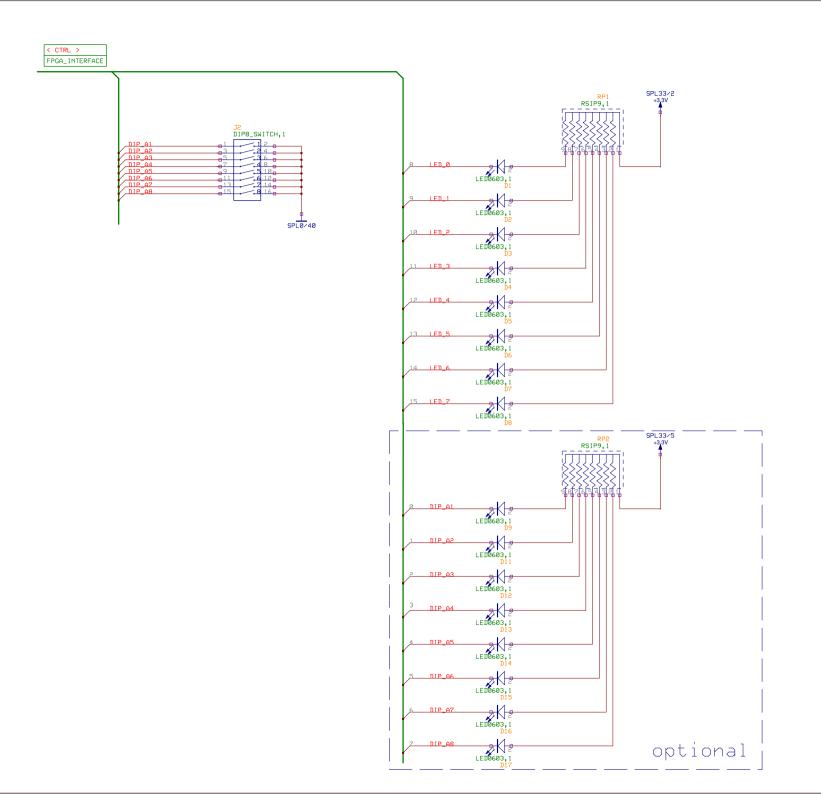
Project: RL Disk Emulator

Author: A.Mann

Date: 11.06.2017 Page: 1 / 7

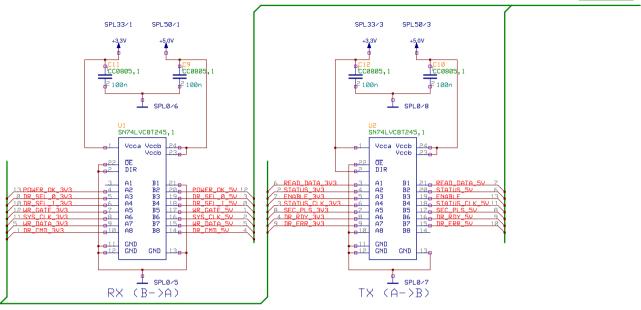


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|----------------------------|------------------|-------|-------|
| Title: | FPGA Interface | | |
| Project: | RL Disk Emulator | | |
| Author: | A.Mann | | |
| Date: | 11.06.2017 | Page: | 2 / 7 |



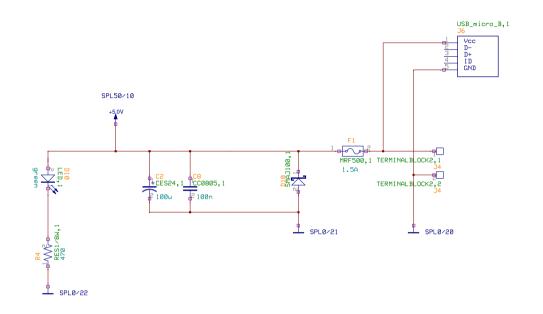
| computer | computermuseum muenchen | | | | |
|----------|----------------------------|--|--|--|--|
| Title: | Control Switches, LEDs | | | | |
| Project: | RL Disk Emulator | | | | |
| Author: | A.Mann | | | | |
| Date: | 11.06.2017 Page: 3 / 7 | | | | |



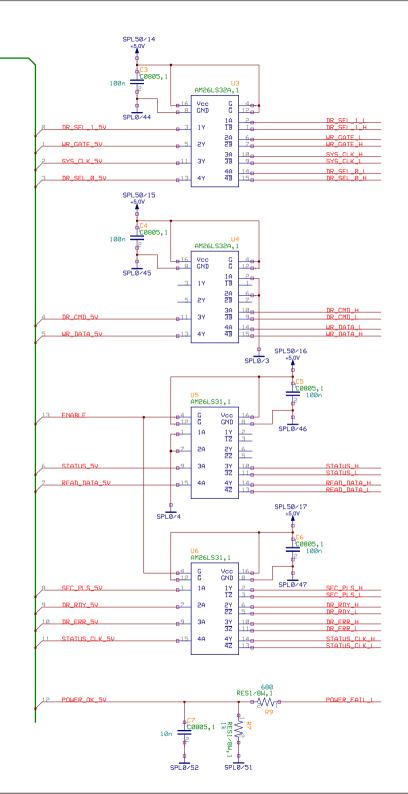


< RL_3V3 >
FPGA_INTERFACE

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|----------|--------------------|-------|-------|
| Title: | Level Shifte | rs | |
| Project: | RL Disk Emul | ator | |
| Author: | A. Mann | | |
| Date: | 11.06.2017 | Page: | 4 / 7 |

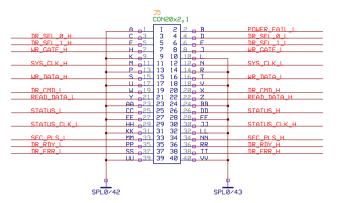


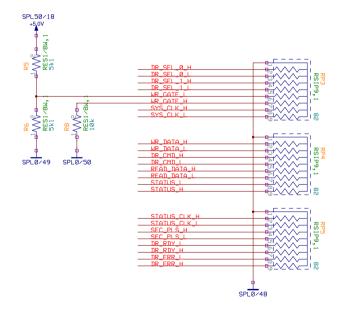
| computer | museum muenchen | | |
|----------|--------------------|-------|-------|
| Title: | Power Input | | |
| Project: | RL Disk Emula | ator | |
| Author: | A. Mann | | |
| Date: | 11.06.2017 | Page: | 5 / 7 |

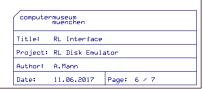


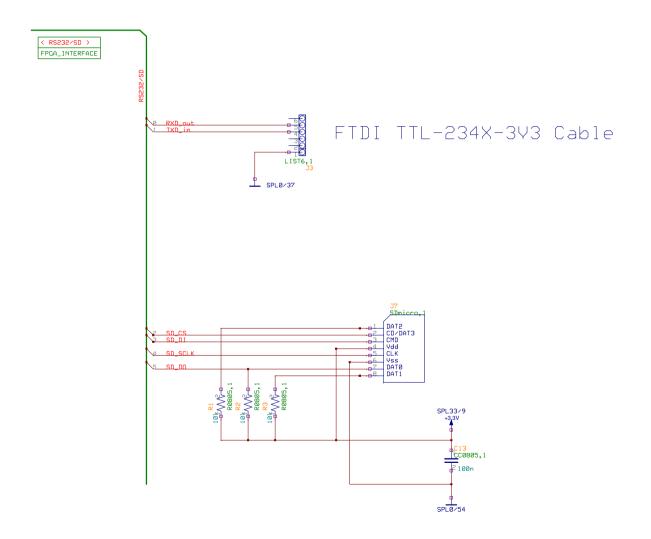
< RL_5V >

LVL_SHIFT









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|----------------------------|--------------------------|--|
| Title: | RS232 Interface, SD Card | |
| Project: | RL Disk Emulator | |
| Author: | A.Mann | |
| Date: | 11.06.2017 Page: 7 / 7 | |

